

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

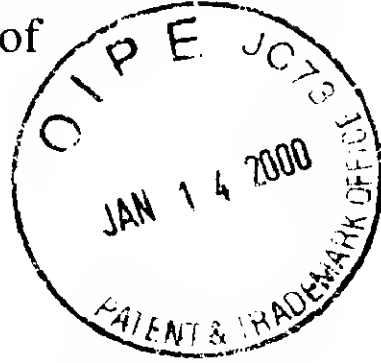
In re PATENT application of

SHUNPEI YAMAZAKI

Serial No. 09/433,705

Filed: 11/04/1999

For: METHOD OF FABRICATING A  
SEMICONDUCTOR DEVICE



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Group Art Unit: 2811

CERTIFICATE OF MAILING

I hereby certify that this  
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INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Japanese Patent Laid-Open No. 07-130652 is in the family of U.S. Patent Application No. 08/633,307, U.S. Patent Application No. 08/928,514, U.S. Patent No. 5,643,826 and U.S. Patent No. 5,923,962.

Japanese Patent Laid-Open No. 10-135468 is in the family of U.S. Patent Application No. 08/951,193.

Japanese Patent Laid-Open No. 10-135469 is in the family of U.S. Patent Application No. 08/951,819.

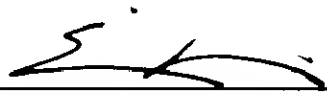
Japanese Patent Laid-Open No. 10-294280 is in the family of U.S. Patent Application No. 09/027,344.

Japanese Patent Laid-Open No. 11-191628 is in the family of U.S. Patent Application No. 09/172,300.

Japanese Patent Laid-Open No. 10-092576 is in the family of WO 9013148, U.S. Patent No. 5,247,190 and U.S. Patent No. 5,399,502.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

  
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